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Application Number	09/966,224
Filing Date	2
First Named Inventor	Michael D. Ruehle
Art Unit	2131 2136
Examiner Name	Not yet assigned COLIN
Attorney Docket Number	42390.P11974

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PTO/SB/08B (10-01)

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Substitute for form 1449B/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	09/966,224
		Filing Date	September 28, 2001
		First Named Inventor	Michael D. Ruehle
		Group Art Unit	2131 2136
		Examiner Name	Not yet assigned CALIN
		Attorney Docket Number	42390.P11974
Sheet	2	of	2

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
ec		Bajard, Jean-Claude et al., "An RNS Montgomery Modular Multiplication Algorithm", Abstract, November 1996, pages 1-10	
ec		Blum, Thomas, "Modular Exponentiation on Reconfigurable Hardware", Thesis submitted to the Faculty of the Worcester Polytechnic Institute, April 8, 1999	
ec		Koc, Cetin Kaya, "RSA Hardware Implementation", Version 1.0, August 1995, pages 1-28	
ec		Rivest, R.L. et al., "A Method for Obtaining Digital Signatures and Public-Key Cryptosystems", Abstract, pages 1-15	

Examiner Signature	<i>David H. Hines</i>	Date Considered	9/16/01
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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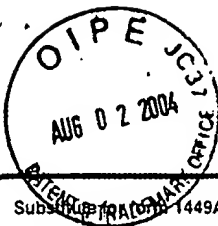
Examiner Signature	<i>Carl. Bl.</i>	Date Considered	9/16/05
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Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zelman (wtr) 08/11/2003.

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Subsidiary Patent 1449A/PTO		Complete If Known	
		Application Number	09/966,224
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Filing Date	September 28, 2001
		First Named Inventor	Michael D. Ruehle
		Art Unit	<del>2131</del> 2136
		Examiner Name	<del>Not Yet Assigned</del> COLIN
		Attorney Docket Number	42390P11974
Sheet	2	of	2

NON PATENT LITERATURE DOCUMENTS			
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cc	3	KATO T ET AL: "A Design for Modular Exponentiation Coprocessor in Mobile elecommunication Terminals", Cryptographic Hardware and Embedded Systems. 2nd International Workshop, CHES 2000, Worchester, MA, Aug. 17-18, 2000, pages 516-228, XP001049135.	
cc	4	BLUM ET AL: "High-Radix Montgomery Modular Exponentiation on Reconfigurable Hardware, IEEE Trans. Comput. (USA), IEEE Transaction on Computers, July 2001, IEE, USW, vol 50, no. 7, July 2001, pages 759-765, XP002283657.	
cc	5	IWAMURA K ET AL: "Montgomery Modular-Multiplication Method and Systolic Arrays Suitable for Modular Exponentiation", Electronics & Communications in Japan, Part III - Fundamental Electronic Science Scripta Technica. New York, US, vol 77, no 3, March 1, 1994, pages 40-50, XP000468346.	

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